

ABSTRACT

An integrated circuit (IC) device that has an analog front end with an I/O buffer is reset. The I/O buffer has a driver circuit to transmit a stream of information over a serial point to point link, and a receiver circuit to receive a stream of information over the link. Digitally-controllable transmission line terminations are provided for the driver and receiver circuits, respectively. A digitally-controllable reference signal level is also provided for the I/O buffer. A number of impedance matching compensation values are automatically calibrated against one or more reference resistors, by calibrating a first value and then a second value, and a third value. These calibrated values are automatically applied to set the reference signal level, driver termination, and receiver termination, respectively. Other embodiments are also described and claimed.